

<b>Notice of References Cited</b>		Application No. <b>08/720,693</b>	Applicant(s) <b>Kao et al</b>
		Examiner <b>George Fourson</b>	Group Art Unit <b>2823</b>

**U.S. PATENT DOCUMENTS**

	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS
A					
B					
C					
D					
E					
F					
G					
H					
I					
J					
K					
L					
M					

**FOREIGN PATENT DOCUMENTS**

	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUBCLASS
N						
O						
P						
Q						
R						
S						
T						

**NON-PATENT DOCUMENTS**

	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
U	Wolf, S. et al, "Silicon Processing For the VLSI Era: Vol.1, Process Technology", Lattice Press, pp.323-324	1986
V		
W		
X		